

Parameters extraction technique for optimal network functions of SC circuits

Vladimir Filaretov

Department of Electrical
Engineering
Ul'yanovsk State Technical
University Ul'yanovsk, Russia
e-mail: vvfil@mail.ru

Konstantin Gorshkov

Department of Electrical
Engineering
Ul'yanovsk State Technical
University Ul'yanovsk, Russia
e-mail: K.Gorshkov@ulstu.ru

Sergey Kurganov

Department of Electrical
Engineering
Ul'yanovsk State Technical
University Ul'yanovsk, Russia
e-mail: sakurganov@mail.ru

Abstract — The parameter extraction technique for symbolic analysis of switched capacitors circuits have been presented. The proposed extraction formulae reduce of cancellation sum-of-product terms amount in transfer function expression. For equivalent circuits of SC the models based on voltage controlled current sources with capacities parameters have been used to avoid the cancellations appearance at the circuit level. The proposed technique is implemented in symbolic circuit analysis program Cirsym. Illustrative examples on SC circuit symbolic analysis are given.

Key words: switched-capacitor, symbolic analysis, circuit determinant, nullor, n-poles extraction.

I. INTRODUCTION

Time-discrete circuits containing switched-capacitors (SC) are widely used in mixed-signal systems, such as electronic filters, Sample-Hold circuits, peak detectors, mixers, modulators of shift-keying techniques, analog-digital or digital-analog converters [1, 2].

The Spice analysis of switched circuits is very laborious and time consuming. It consists in the steady state transient analysis with the input signal swept by a harmonic signal of a given frequency, the selection of an AC component of output signal, and its comparison with the input. This procedure must be repeated for more frequencies.

Symbolic analysis in contrast to numeric calculation techniques provides the way to get analytical solution for transfer function computation, for determination of required network mode conditions, for fault modeling and testing of SC circuits [3-12]. The symbolic techniques are employing the ideal models of circuit elements: capacitors, ideal switches (short-circuited connection is corresponding for on-state, open circuit is corresponding for off-state), controlled sources and ideal operational amplifiers. The network equations derived from the SC equivalent circuit are the z -domain charge equations, where z is the operator of the z -transform [13].

For symbolic analysis of the SC circuit the methods of nodal charge equations using matrix calculus are usually applied. Some of them are based on nodal analysis, such as technique proposed in [10], the others have deal with the modified nodal matrix [5, 6]. But matrix model of SC circuit

consist of the equal summands with opposite sign. It leads to appearing of the cancellation sum-of-product terms.

The topological methods are implementing graph theory for symbolic circuit analysis. The techniques based on the signal flow graph [4, 7, 8], Mason-Coates graph [3, 11], and Nathans graph [9] have been developed. The main drawback of topological approach is a time-consuming combinatorial search of branches and loops. The appearing of cancellation terms is also unavoidable for topological methods.

In this paper we propose the SC circuit symbolic calculation technique based on the generalized parameter extraction method (GPEM) which is cancellation-free [14-18]. In Section II we explain the basic idea of SC circuit analysis based on network determinant expansion. The comparison of the network function evaluation by means of proposed technique and matrix-based approach are presented in Sections III and IV.

II. DESCRIPTION OF THE METHOD

A. The basics of generalized parameter extraction method

Let's express the transfer function of electronic network as

$$H = N / D, \quad (1)$$

where N is the determinant of the network, in which the independent source and output response are replaced by nullor, and D is the determinant of the network, in which the independent excitation and the output response are zero.

For network determinants calculation the parameter extraction formula was presented in [14]:

$$\Delta = \chi \Delta(\chi \rightarrow \infty) + \Delta(\chi = 0), \quad (2)$$

where χ is a parameter of linear circuit element, $\Delta(\chi \rightarrow \infty)$ and $\Delta(\chi = 0)$ are the determinants of the circuits in which the parameter of extracted element $\chi \rightarrow \infty$ or $\chi = 0$ respectively.

For nullor extraction the special formula must be used:

$$\Delta = \pm \Delta_n, \quad (3)$$

where Δ_n is the determinant of the circuit after the nullor number n extraction.

This work is supported by the Russian Foundation for Basic Research (RFBR) under grant No. 15-07-05847.

The procedure of nullor extraction can be formalized by the following steps [18]:

1. The choice of the supporting nodes. First of the supporting nodes should be connected to norator, and the second to nullator. If there is a common node of a nullator and a norator it must be chosen as single supporting node. Note that supporting node may correspond to the ground node.

2. The terminals of non-extracted norators and current sources connected to supporting node are moved to the opposite node of extracted norator. In that case the non-extracted nullators and controlling voltages keep connections to supporting node. Then in the same way, the terminals of non-extracted nullators and controlling voltages connected to supported node are moved to the opposite node of extracted nullator. In that case non-extracted norators and current sources keep connections to supporting node.

3. A norator and a nullator of the extracted nullor are deleted from circuit. In case of two supporting nodes they must combine.

4. If extracted norator and nullator have got the same orientation with respect to the supporting node then the determinant sign will be positive. Otherwise the sign will be negative. In case of two supporting nodes the inverted rule is needed.

Other special cases of circuit topology transformations provided by GPME can be found in [14-18].

B. The GPME-based technique for SC circuits symbolic analysis

For symbolic analysis of SC circuits in the z -domain the clock-free equivalent network of switched capacitor is needed. In the Table I the equivalent networks for typical cases of SC configurations are presented. We used here the SC model based on the usage of voltage controlled current source (VCCS) with capacities parameter [3] instead of common model with two-port complex capacities.

The symbolic expressions of switched circuits transfer functions are calculating by means of circuit-algebraic formulae as well as for analog networks [14]. Of course it is important to take into account that SC equivalent circuit consists of the out-of-phase input and output. As example in Table II we present the circuit-algebraic formulae for analysis of the three-terminal circuit with two-phase control. For that case four voltage transfer function variations different by phases of input and output voltage have been derived. In case of circuit controlled by more than two phases the amount of circuit-algebraic formulae will increase correspondingly.

The capacitance extraction formula easy developed from expression (2) in consequence of current and charge compatibility:

$$\Delta = C \Delta(C \rightarrow \infty) + \Delta(C = 0). \quad (4)$$

For extraction of z -capacitance just replacement of parameter C by C_z in expression (4) is needed:

$$\Delta = C_z \Delta(C_z \rightarrow \infty) + \Delta(C_z = 0). \quad (5)$$

For the controlled source parameter extraction the formula (2) must be used.

TABLE I. THE EQUIVALENT NETWORKS FOR TYPICAL CASES OF SC CONFIGURATIONS

	SC topology	Complex equivalent circuit (operator $s = z^{-1/2}$)
1	Switched capacitor in general form	
2	Grounded SC for voltage transfer	
3	SC with single-channel voltage transfer	
4	Non-grounded SC for voltage transfer	
5	SC with single switch	

III. EXAMPLE OF THE SOLVING

A. Solving by GPME-based technique

Let's consider the implementation of proposed technique to network function calculation of simple SC circuit presented in Fig. 1 (a). The z -equivalent circuit shown in Fig. 1 (b) where $s = 1 - z^{-1}$, $p = 1 - s^2$ is formed by means of Table I.

IV. AUTOMATIC SYMBOLIC ANALYSIS OF SC CIRCUITS

For automated analysis of large-scale SC circuits the computer program Cirsym developed by V. Filaretov can be used [14]. Cirsym is a symbolic analyzer a part of the software tool SCADS. The program can be downloaded from www.intersyn.net. The input data for program is SPICE-like circuit description. Note that in case of SC circuits the capacitor with parameter C and controlled source with capacities parameter must be presented as g - conductance and G - conductance correspondingly. Of course all parameter indexes are still the same. The operator $z^{(-1/2)}$ is considered as equivalent to complex operator s used in program. The symbolic expressions obtained by means of Cirsym needs to be transformed in z -domain by means of simple replacement of parameters g and G by C and $Cz^{(-1/2)}$ correspondingly.

Let's consider the automated symbolic analysis of Fleischer-Laker biquad with two ideal OpAmp and twelve capacitors presented in Fig. 3. The equivalent circuit with VCCS-based SC models (see Table I) can be seen in Fig. 4. Note that controlling branches of controlled sources are not shown for purpose of presentation clearness.

We used the program Cirsym for calculation of transfer function for odd-phase of input and output voltages. The result expression in which the each capacities parameter is represented by corresponding index is listed below:

$$H_{\text{cirsym}} := \frac{s^2 a^2 (s^2 (-s^2 d^2 h^2 b^2 (l+k) + d^2 (-s^2 e^2 (j+h)^2 k + b^2 (-s^2 l^2 (k-l) + (l+k)^2 l + (l+j+h+k)^2 g)) + d^2 s^2 e^2 (j+h)^2 k + s^2 j^2 d^2 b^2 (-k-l) (-s^2 + 1) + s^2 b^2 d^2 (j+h)^2 k^2 (s^2 - 1) - s^2 d^2 b^2 (-s^2 (k^2 l + k^2) + (l+j+h+k)^2 (k+i)) + d^2 b^2 (-s^2 k^2 (l+k) + (l+j+h+k)^2 (k+i)))}{((l+k+j+h)^2 (s^2 a^2 d^2 b^2 (c+e^2 (1-s^2)) - s^2 b^2 d^2 (-s^2 + 1) - b^2 (f+b)^2 d^2 (s^2 + 1))}; \quad (11)$$

We can simplify the expression (11) by means of 22 cancellation terms reduction as following:

$$H := \frac{(((k^2 + (h+l+2j)^2 k + l^2 j)^2 d^2 a^2 (l+h)^2 (l+k)^2 b^2 a^2 k^2 e^2 (j+h)^2 s^4 + ((-2k^2 + (-2h-2l-3j)^2 k + (-j-i)^2 l - i^2 (j+h)) d^2 a^2 ((l+c)^2 k + l^2 + 1^2 c + c^2 (j+h)))^2 b^2 a^2 k^2 e^2 (j+h)^2 s^2 + d^2 b^2 (k+i)^2 (l+j+h+k))}{((l+j+h+k)^2 b^2 ((a^2 e - d^2 b^2) s^4 + (2d^2 b^2 + d^2 f a^2 (e+c)) s^2 - (f+b)^2 d^2)}; \quad (12)$$

Let's compare the expression (12) with determinant of Fleischer-Laker biquad circuit calculated by means of BDD-approach:

$$H_{\text{BDD}} := \frac{(h+l+k+j)^2 (-g-l)^2 s^2 d^2 a^2 b^2 + (h+l+k+j)^2 s^2 l^2 d^2 a^2 b^2 + (h+l+k+j)^2 (-i-k)^2 d^2 b^2 - (h+l+k+j)^2 (-i-k)^2 s^2 d^2 b^2 + (h+l+k+j)^2 s^2 k^2 d^2 b^2 - (h+l+k+j)^2 s^2 k^2 d^2 e^2 a^2 - (h+l+k+j)^2 s^3 k^2 d^2 b^2 + (h+l+k+j)^2 s^4 k^2 d^2 e^2 a^2 - s^2 (h+l)^2 s^2 (-l-k)^2 s^2 d^2 a^2 b^2 + s^2 (-l-k)^2 d^2 a^2 b^2 - s^2 (j+k)^2 s^2 (-l-k)^2 d^2 b^2 + s^2 (j+k)^2 s^2 (-l-k)^2 d^2 b^2 + k^2 s^2 (-l-k)^2 d^2 e^2 a^2 - k^2 s^3 (-l-k)^2 d^2 b^2 + k^2 s^4 (-l-k)^2 d^2 e^2 a^2}{((h+l+k+j)^2 (-d^2 (-f-b)^2 b^2 - d^2 s^2 a^2 e^2 b^2 + d^2 s^2 a^2 e^2 b^2 + s^2 d^2 (-f-b)^2 b^2 + s^2 d^2 b^2 - s^2 d^2 a^2 (-c-e)^2 b^2 s^4 d^2 a^2 e^2 b^2)}; \quad (13)$$

As can be seen the amount of cancellation terms is increase to 32. The appearance of new 10 cancellation terms is a consequence of matrix calculus because switching capacitor matrix model include of some equal parameters.

In Table III we present the comparison of cancellation terms amounts in expressions obtained by means of GPEM-based technique and BDD method. We performed the calculation for VCCS-based equivalent circuit of Fleischer-Laker biquad and for circuit model based on two-ports.

TABLE III. COMPARISON OF TERMS AMOUNTS IN TRANSFER FUNCTION EXPRESSIONS OF FLEISCHER-LAKER BIQUAD

	Circuit model	Calculation technique	Summands amount		Non-cancellation summands amount		Cancellation summands amount
			N	D	N	D	
1	VCCS-based circuit	GPEM	42	36	42	36	22
2		Matrix	66	44			32
3	circuit based on two-ports	GPEM	282	168			372
4		Matrix	420	360			702

It is possible to use the simplified SC models for symbolic expressions calculation of compact size without cancellations. But approximation may leads to significant lack of accuracy of network function. For example let's consider the network function of Fleischer-Laker biquad circuit presented in [5]:

$$H_{\text{simplified}} := \frac{(d^2 k + d^2 j - a^2 l - a^2 h) s^4 + (-2d^2 k + a^2 l + a^2 g - d^2 j - d^2 i) s^2 + d^2 (k+i)}{((a^2 e - d^2 b^2) s^4 + (-a^2 e + 2d^2 b^2 - a^2 c + d^2 f) s^2 - d^2 (f+b))}; \quad (14)$$

This function is exactly corresponding to single-channel equivalent circuit shown in Fig. 5.

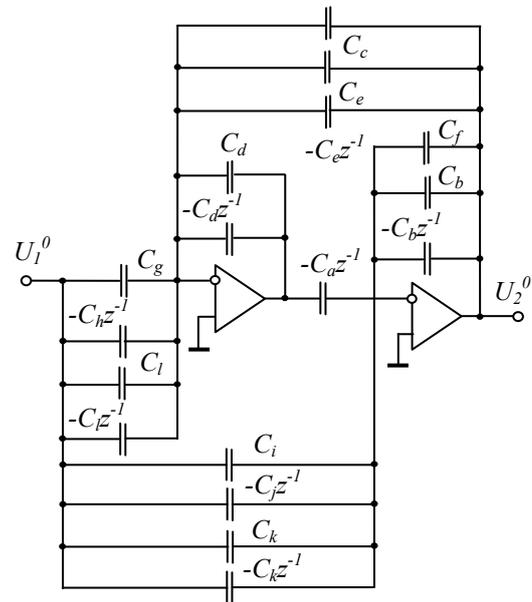


Fig. 5. Single-channel equivalent circuit of Fleischer-Laker biquad

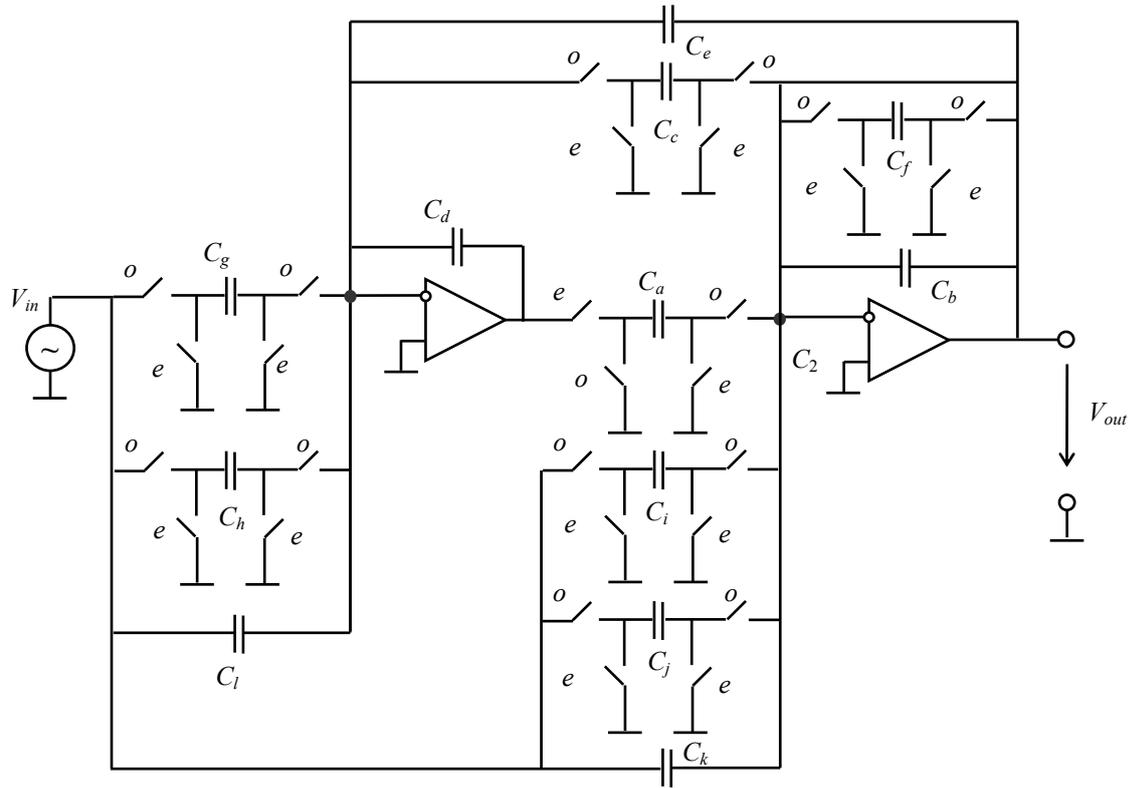


Fig. 3. Switched-capacitor biquad of Laker and Fleischer

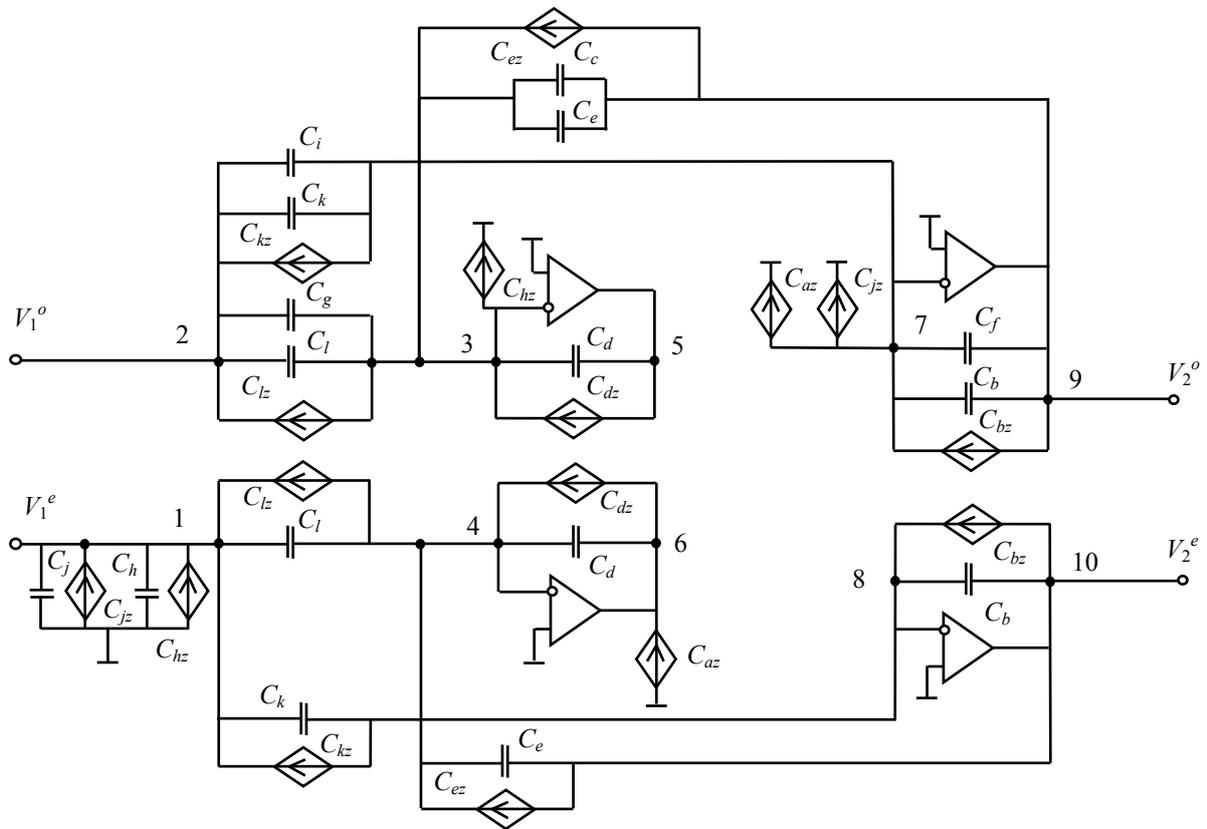


Fig. 4. VCCS-based equivalent circuit of Fleischer-Laker biquad

We performed the comparison of accuracy between expressions (12) and (14) for clock frequency of 10 kHz. Using Maple 18 software simulation results versus frequency f are shown in Fig. 6. The values of parameters elements are: $C_a=276\text{ pF}$; $C_b=276\text{ pF}$; $C_c=4\text{ pF}$; $C_d=276\text{ pF}$; $C_e=45\text{ pF}$; $C_f=2\text{ pF}$; $C_g=4\text{ pF}$; $C_h=2\text{ pF}$; $C_i=1\text{ pF}$; $C_j=3\text{ pF}$; $C_k=C_l=2\text{ pF}$. In that case the biquad circuit operates like low-pass filter with cutoff frequency of 190 Hz.

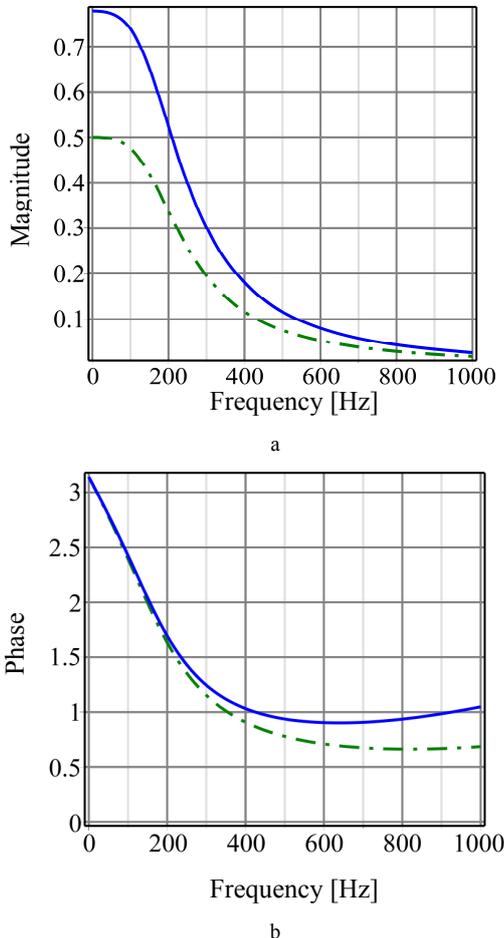


Fig. 6. Simulation of Fleischer-Laker biquad transfer functions (12) obtained by means of GPEM-based technique (blue line) and (14) presented in [5] (green dash dot line) versus frequency f

The result of transfer function (12) simulation is in good agreement with the numerical response computed by SPICE. But the amplitude-frequency characteristic error of the simplified equivalent circuit presented in [5] is more than 30%.

V. CONCLUSIONS

The technique for SC circuits symbolic analysis based on generalized parameter extraction method have been proposed. The GPEM approach does not require the matrix formation, which include a lot of the equal summands with opposite sign. The further reduce of cancellation provided by usage of VCCS-based equivalent circuit instead of common SC model with

two-port complex capacities. The process of calculation symbolic network functions of SC circuits is automated by computer program Cirsym.

REFERENCES

- [1] P. E. Fleischer and K. R. Laker, "A family of active switched-capacitor biquad building blocks," *The Bell System Technical Journal*, vol. 58, no. 10, pp. 2235-2269, 1979.
- [2] P. Allen and E. Sanchez-Sinencio. *Switched capacitor circuits*. New York: VNR, 1984.
- [3] A. Konczykowska and M. Bon, "Topological analysis of switched-capacitor networks," *Electronics letters*, vol. 16, no. 3, pp. 2-3, 1980.
- [4] J.J. Mulawka, G.S. Moschytz, "A by-inspection analysis of SC networks using direct topological rules," *IEE Proc. Pt. G. Electron. Circuits and systems*, vol. 132, no 6, pp. 255-265, December 1985
- [5] G.G.E. Gielen, W. Sansen, and H.C.C. Walscharts, "ISAAC: A Symbolic Simulator for Analog Integrated Circuits," *IEEE Journal of Solid-State Circuits*, vol. 24, no. 6, pp. 1587 - 1597, December 1989.
- [6] V.A. Zivkovic, P.M. Petkovic, and D.P. Milovanovic, "Automatic symbolic analysis of SC networks using a modified nodal approach," in *Proc. of 21st International Conference on Microelectronics*, vol. 2, pp. 717 - 720, 1997.
- [7] M. H. Fino and L. J. Mourão, "SymbSI- a program for the symbolic signal flow graph generation of switched current circuits," in *Proc. of IEEE International Conference on Electronics, Circuits and Systems*, Vol.3, pp. 2-5, 1998.
- [8] T.M. Lee and E. Chua "Analysis of switched capacitor filters using ACCUSIM," Deep Submicron Technical Publication (www.mentor.com/dsm), 2001, 7 p.
- [9] A.S. Korotkov and D.V. Morosov "Topological analysis of continuous and discrete time linear circuits using Nathan rules," *I Proc. of Int. Conf. Symbolic methods and applications to circuits design (SMACD 2002)*, Sinaia, Romania, pp.35-42, October 2002.
- [10] J. Hospodka, P.Sovka, and B. Psenicka, "Design and realization of a filter bank by switched capacitor technique," in *Proc. of 20th European Conference on Circuit Theory and Design (ECCTD-2011)*, Linköping, Sweden, pp. 753 - 756, August 2011.
- [11] B. Brtnik, "Solving switched capacitors circuits by full graph methods," *International Journal of Circuits, Systems and Signal Processing*, vol. 5, no. 3, pp. 271-278, 2011.
- [12] J. Cheng, G. Shi, A. Tai, and F. Lee, "Symbolic fault modeling for switched-capacitor circuits," *2013 IEEE Int. Conf. IEEE Reg. 10 (TENCON 2013)*, pp. 1-4, Oct. 2013.
- [13] E. I. Jury. *Theory and Applications of the Z-Transform Method*. New York: Wiley, 1964.
- [14] V. V. Filaretov and A. S. Korotkov, "Generalized parameter extraction method in symbolic network analysis," in *Proc. of ECCTD, Kraków, Poland*, vol. 2, pp. 406-409, Sept. 2003.
- [15] V. V. Filaretov and A. S. Korotkov, "Generalized parameter extraction method in case of multiple excitation," in *Proc. 8th Int. Workshop on Symbolic methods and applications to circuit design (SMACD'04)*, Wrocław, Poland, pp.8-11, Sept. 2004.
- [16] V. V. Filaretov and K. S. Gorshkov, "The Generalization of the Extra Element Theorem for symbolic circuit tolerance analysis," *Journal of Electrical and Computer Engineering*, vol. 2011, Article ID 652706, 5 p, April 2011.
- [17] V. V. Filaretov and K.S. Gorshkov, "Topological analysis of active networks containing pathological mirror elements," in *Proc. of IEEE XXXIII International Scientific Conference "ELECTRONICS AND NANOTECHNOLOGY" (ELNANO-2013)*, Kiev, Ukrain, pp. 293-296, April 2013.
- [18] V. Filaretov, K. Gorshkov, and S. Kurganov, "A Cancellation-Free Symbolic Sensitivity Technique Based on Network Determinant Expansion," *Adv. Electr. Eng.*, vol. 2015, pp. 1-13, 2015.